



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kevin Sallese  
Assignee: Lattice Semiconductor Corporation  
Title: Programmable Logic Device With A Memory-Based Finite State Machine  
Serial No.: 10/624,965 Filing Date: July 21, 2003  
Examiner: Chang, Daniel D. Group Art Unit: 2819  
Docket No.: M-15170 US Confirmation No.: 6289

Irvine, California  
March 23, 2005

Via Facsimile to (703) 746-4000

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.312

Dear Sir:

In response to the Notice of Allowance dated January 4, 2005, Applicant submits the following amendments and remarks.

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